



EXAMINATION BRANCH

TIME-TABLE

TIME: 02.30 P.M TO 04.30 P.M

M.TECH. I SEM. (RA18 REGULATION – 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, FEBRUARY - 2021

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
25.02.2021 (THURSDAY)	Special Machines	Digital System Design	Digital System Design	Computer Integrated Manufacturing	Advanced Data Structures
27.02.2021 (SATURDAY)	Analysis of Power Electronic Converters	RTL Simulation and Synthesis with PLDs	RTL Simulation and Synthesis with PLDs	Advanced Computer Aided Design & Manufacturing	Machine Learning
02.03.2021 (TUESDAY)	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR
04.03.2021 (THURSDAY)	Power Electronic Control of DC Drives	--	Analog Integrated Circuit Design	Advanced Material Technology	Distributed Systems
06.03.2021 (SATURDAY)	--	Embedded C	Embedded C	--	Software Process & Project Management
09.03.2021 (TUESDAY)	HVDC Transmission	ARM Architectures	--	Special Manufacturing Process	--

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

ADDL. CONTROLLER OF EXAMINATIONS
Dt.: 04.02.2021

CONTROLLER OF EXAMINATIONS



EXAMINATION BRANCH

TIME-TABLE

TIME: 02.30 P.M TO 04.30 P.M

M.TECH. I SEM. (RA15 REGULATION – 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, FEBRUARY - 2021

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
25.02.2021 (THURSDAY)	Machine Modelling & Analysis	<u>Elective – II</u> Digital System Design	<u>Elective – I</u> Digital System Design	<u>Elective – II</u> Precision Engineering	Big data and Analytics	Software Requirements and Estimation
27.02.2021 (SATURDAY)	Analysis of Power Electronic Converters	<u>Elective – I</u> VLSI Technology and Design	VLSI Technology and Design	Advanced Computer Aided Design & Manufacturing	Machine Learning	Web Technologies and Services
02.03.2021 (TUESDAY)	Modern Control Theory	Real Time Operating System	CMOS Analog Integrated Circuit Design	Theory of Metal Cutting	<u>Elective – II</u> Software Architecture and Design Patterns	<u>Elective – I</u> Information Security and Audit
04.03.2021 (THURSDAY)	Power Electronic Control of DC Drives	Embedded System Design	Embedded System Design	<u>Elective – III</u> Applied Tribology	Distributed Systems	Software Development Methodologies
06.03.2021 (SATURDAY)	<u>Elective – II</u> Dynamics of Electrical Machines	<u>Elective – III</u> TCP/IP Networks	<u>Elective – II</u> CPLD and FPGA Architectures and Applications	Automation in Manufacturing	<u>Elective – I</u> Software Process and Project Management	<u>Elective – II</u> Cyber Security and Cyber Laws
09.03.2021 (TUESDAY)	<u>Elective – I</u> High Voltage DC Transmission	ARM Architectures	<u>Elective – III</u> Advanced Data Communications	<u>Elective – I</u> Special Manufacturing Process	Advanced Data Structures and Algorithms	Advanced Data Structures and Algorithms

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ADDL. CONTROLLER OF EXAMINATIONS
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EXAMINATION BRANCH

TIME-TABLE

TIME: 10.30 A.M TO 12.30 P.M

M.TECH. II SEM. (RA18 REGULATION – 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, FEBRUARY - 2021

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
26.02.2021 (FRIDAY)	Power Electronic Control of AC Drives	Embedded OS & RTOS	CMOS VLSI Design	Manufacturing Systems Simulation	Advanced Algorithms
01.03.2021 (MONDAY)	Power Electronic Applications to Renewable Energy	--	ASIC and SOC	Theory of Metal Cutting and Forming	Big Data Analytics
03.03.2021 (WEDNESDAY)	Microprocessor and Microcontroller	Advanced Digital Signal Processing	Advanced Digital Signal Processing	Advanced Tool Design	Deep Learning
05.03.2021 (FRIDAY)	--	Embedded Networking	Embedded Networking	NDE & NDT	--
		High Speed Digital Design	High Speed Digital Design		
08.03.2021 (MONDAY)	Power Quality	System On Chip Architecture	--	--	Cryptography and Network Security
	Dynamics of Electrical Machines				

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ADDL. CONTROLLER OF EXAMINATIONS
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EXAMINATION BRANCH

TIME-TABLE

TIME: 10.30 A.M TO 12.30 P.M

M.TECH. II SEM. (RA15 REGULATION – 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, FEBRUARY - 2021

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
26.02.2021 (FRIDAY)	Power Electronic Control of AC Drives	CPLD and FPGA Architectures and Applications	Real Time Operating Systems	Manufacturing Systems Simulation Modelling & Analysis	Advanced Network Programming	Software Process and Project Management
01.03.2021 (MONDAY)	Neural And Fuzzy Systems	Hardware Software Co-Design	CMOS Mixed Signal Circuit Design	Quality Engineering in Manufacturing	High Performance Computing	Big Data and Analytics
		Network Security and Cryptography				
03.03.2021 (WEDNESDAY)	Microprocessor and Microcontroller	Digital Signal Processors and Architectures	Embedded C	Materials Technology	Adhoc and Sensor Networks	Scripting Languages
			Digital Signal Processors and Architectures		Database Security	
05.03.2021 (FRIDAY)	Flexible AC Transmission Systems (FACTS)	Embedded Networking	Embedded Networking	Production & Operations Management	Web Services and Service Oriented Architecture	Cloud Computing
08.03.2021 (MONDAY)	Power Quality	System On Chip Architecture	Ad-hoc Wireless and Sensor Networks	Concurrent Engineering & Product Life Cycle Management	Software Quality Assurance & Testing	Software Quality Assurance and Testing
		Sensors and Actuators	Sensors and Actuators			
10.03.2021 (WEDNESDAY)	Reliability Engineering	Device Modelling	Low Power VLSI Design	Design and Manufacturing of MEMS & Micro Systems	Number theory and Cryptography	Software Architecture and Design Patterns
		Wireless LANs and PANs				

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ADDL. CONTROLLER OF EXAMINATIONS
Dt.: 04.02.2021

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EXAMINATION BRANCH

TIME-TABLE

M.TECH. III SEMESTER (RA18 REGULATION – 2018, 2019 BATCHES) REGULAR & SUPPLEMENTARY EXAMINATIONS, FEBRUARY - 2021

TIME: 10.30 A.M TO 12.30 P.M

Date & Day	SUBJECTS (RA18 – 2018, 2019 Admitted Batches)				
	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	COMPUTER SCIENCE & ENGINEERING	ADVANCED MANUFACTURING SYSTEMS
25.02.2021 (THURSDAY)	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	DESIGN FOR TESTABILITY	DESIGN FOR TESTABILITY	NATURAL LANGUAGE PROCESSING	MANUFACTURING POLYMERS
27.02.2021 (SATURDAY)	OPERATIONS RESEARCH	OPERATIONS RESEARCH	OPERATIONS RESEARCH	CYBER LAWS	INDUSTRIAL SAFETY
	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY		

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Dt.: 04.02.2021

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