

TIME-TABLE

TIME: 10.00 AM TO 01.00 PM

M.TECH. I SEM. (RA18 REGULATION - 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, AUGUST - 2021

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
03.08.2021 (TUESDAY)	Special Machines	Digital System Design	Digital System Design	Computer Integrated Manufacturing	Advanced Data Structures
05.08.2021 (THURSDAY)	Analysis of Power Electronic Converters	RTL Simulation and Synthesis with PLDs	RTL Simulation and Synthesis with PLDs	Advanced Computer Aided Design & Manufacturing	Machine Learning
07.08.2021 (SATURDAY)	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR
10.08.2021 (TUESDAY)	Power Electronic Control of DC Drives	-	Analog Integrated Circuit Design	Advanced Material Technology	Distributed Systems
12.08.2021 (THURSDAY)	-	Embedded C	Embedded C	_	Software Process &Project Management
16.08.2021 (MONDAY)	HVDC Transmission	ARM Architectures	-	Special Manufacturing Process	

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ADDL. CONTROLLER OF EXAMINATIONS

Dt.: 16.07.2021



TIME-TABLE

TIME: 10.00 AM TO 01.00 PM

M.TECH. I SEM. (RA15 REGULATION - 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, AUGUST - 2021

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTE MS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
03.08.2021 (TUESDAY)	Machine Modelling & Analysis	Elective – II Digital System Design	Elective – I Digital System Design	Elective – II Precision Engineering	Big data and Analytics	Software Requirements and Estimation
05.08.2021 (THURSDAY)	Analysis of Power Electronic Converters	Elective – I VLSI Technology and Design	VLSI Technology and Design	Advanced Computer Aided Design & Manufacturing	Machine Learning	Web Technologies and Services
07.08.2021 (SATURDAY)	Modern Control Theory	Real Time Operating System	CMOS Analog Integrated Circuit Design	Theory of Metal Cutting	Elective – II Software Architecture and Design Patterns	Elective — I Information Securit and Audit
10.08.2021 (TUESDAY)	Power Electronic Control of DC Drives	Embedded Syste m Design	Embedded System Design	Elective – III Applied Tribology	Distributed Systems	Software Development Methodologies
12.08.2021 (THURSDAY)	Elective – II Dynamics of Electrical Machines	Elective – III TCP/IP Networks	Elective – II CPLD and FPGA Architectures and Applications	Automation in Manufacturing	Elective – I Software Process and Project Management	Elective – II Cyber Security and Cyber Laws
16.08.2021 (MONDAY)	Elective – I High Voltage DC Transmission	ARM Architectures	Elective – III Advanced Data Communications	Elective – I Special Manufacturing Process	Advanced Data Structures and Algorithms	Advanced Data Structures and Algorithms

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M.TECH. II SEM. (RA15 REGULATION - 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, AUGUST - 2021

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
04.08.2021 (WEDNESDAY)	Power Electronic Control of AC Drives	CPLD and FPGA Architectures and Applications	Real Time Operating Systems	Manufacturing Systems Simulation Modelling & Analysis	Advanced Network Programming	Software Process and Project Management
06.08.2021 (FRIDAY)	Neural And Fuzzy Systems	Hardware Software Co-Design Network Security and Cryptography	CMOS Mixed Signal Circuit Design	Quality Engineering in Manufacturing	High Performance Computing	Big Data and Analytics
09.08.2021 Microprocessor and Microcontroller	Digital Signal	Embedded C		Adhoc and Sensor Networks	Scripting Languages	
	Processors and Architectures	Digital Signal Processors and Architectures	Materials Technology	Database Security		
11.08.2021 (WEDNESDAY)	Flexible AC Transmission Systems (FACTS)	Embedded Networking	Embedded Networking	Production & Operations Management	Web Services and Service Oriented Architecture	Cloud Computing
13.08.2021 (FRIDAY)	Power Quality	System On Chip Architecture	Ad-hoc Wireless and Sensor Networks	Concurrent Engineering & Product Life Cycle Management	Software Quality	Software Quality
		Sensors and Actuators	Sensors and Actuators		Assurance & Testing	Assurance and Testing
17.08.2021 (TUESDAY)	Reliability Engineering	Device Modelling	Low Power VLSI	Design and Manufacturing of MEMS & Micro Systems	Number theory and Cryptography	Software Architecture and Design Patterns
		Wireless LANs and PANs	Design			

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Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
04.08.2021 (WEDNESDAY)	Power Electronic Control of AC Drives	Embedded OS & RTOS	CMOS VLSI Design	Manufacturing Systems Simulation	Advanced Algorithms
06.08.2021 (FRIDAY)	Power Electronic Applications to Renewable Energy		ASIC and SOC	Theory of Metal Cutting and Forming	Big Data Analytics
09.08.2021 (MONDAY)	Microprocessor and Microcontroller	Advanced Digital Signal Processing	Advanced Digital Signal Processing	Advanced Tool Design	Deep Learning
11.08.2021)	Embedded Networking	Embedded Networking		
(WEDNESDAY)		High Speed Digital Design	High Speed Digital Design	NDE & NDT	
13.08.2021 (FRIDAY)	2021 Power Quality	System On Chip Architecture	<u>-</u>		Cryptography and Network Security
	Dynamics of Electrical Machines				

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TIME-TABLE

M.TECH. III SEMESTER (RA18 REGULATION – 2018, 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, AUGUST - 2021

TIME: 02.00 PM TO 05.00 PM

Date & Day	SUBJECTS (RA18 – 2018, 2019 Admitted Batches)						
	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	COMPUTER SCIENCE & ENGINEERING	ADVANCED MANUFACTURIN G SYSTEMS		
03.08.2021 (TUESDAY)	FLEXIBLE AC TRANSMISSIO N SYSTEMS (FACTS)	DESIGN FOR TESTABILITY	DESIGN FOR TESTABILITY	NATURAL LANGUAGE PROCESSING	MANUFACTUR ING POLYMERS		
05.08.2021 (THURSDAY)	OPERATIONS RESEARCH	OPERATIONS RESEARCH	OPERATIONS RESEARCH	CVDED LAVAG	INDUSTRIAL SAFETY		
	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY	CYBER LAWS			

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